

CONTEMPORARY INORGANIC THIN FILM PHOTOVOLTAIC MATERIALS AND TECHNOLOGIES

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Summary: Inorganic thin film photovoltaic materials prove and even more promise that their small consumption and low cost are the drivers of high volume production of solar cells and photovoltaic modules. Representative types of contemporary inorganic thin-film PV materials and technologies are reviewed and their state-of-the-art achievements and future trends presented.

Keywords: photovoltaics, solar cell, semiconductor, inorganic material, thin film.

1. INTRODUCTION

Inorganic thin-film photovoltaic (PV) materials and technologies have gained an important position in the photovoltaic industry and market. Small material consumption and low-temperature deposition processes applicable to large areas in either batch or roll-to-roll production lines make inorganic thin-film solar cells and modules competitive to crystalline silicon solar cells, regarding total costs and state-of-the-art performances [1]. Organic (or plastic) solar cell and dye-sensitized solar cells are part of the emerging PV technologies based on organic semiconductor materials. Organic solar photovoltaic cells consist of conductive organic polymers or small organic molecules for light absorption, charge separation and transport. Such active organic materials are sandwiched between the PV-inactive layers that act as contacts, e.g. a transparent conductive oxide layer as the front contact. In this paper we will limit to and focus on inorganic thin film photovoltaic materials and their solar cell structures, the global market share of which in 2010 was around 15 % (3.2 GW out of 21 GW) and is forecasted to increase to 21% in 2012 [1].

Semiconductor materials used as active layers in inorganic thin film solar cells have amorphous or polycrystalline structure. Silicon-based representatives include hydrogenized amorphous, nanocrystalline or microcrystalline silicon (a-Si:H, nc-Si:H, μ c-Si:H); the main representative of chalcopyrites is polycrystalline Cu(In, Ga)(Se,S)₂ (CIGS), while for II-VI semiconductors it is polycrystalline cadmium telluride (CdTe). Most of them exhibit higher absorption coefficient than crystalline silicon (c-Si

(Fig. 1), thus efficient absorption of solar spectrum may occur in much thinner layers. Thin layers of the materials may be deposited at low temperatures (typically 200 – 500 °C, in cases of a-Si:H even at room temperature) thus allowing the use of different rigid or flexible substrates such as glass, ceramic plates, plastic foils, metal foils, etc [2]. The main advantages of thin-film solar cells are low consumption of materials and energy during their fabrication, low-cost production. Drawbacks of these cells include lower conversion efficiencies [3] and in some cases, light-induced degradation of the materials [4]. For example, the best crystalline silicon cell achieved energy conversion efficiency of 25.0% under standard test conditions (STC) [5], while the record stabilized thin-film amorphous silicon solar cell exhibits 10.1% efficiency under STC [6]. In terms of the conversion efficiency, CdTe solar cells with the record efficiency of 16.7% [7] and CIGS solar cells with the record efficiency of 20.3% [8] are better representatives of the thin-film solar cells. In the following sections we will review individual types of inorganic thin-film PV materials and technologies and their state-of-the-art achievements.

2. THIN FILM SILICON

Apart from crystalline silicon, which is usually solidified in ingots and cut in wafers, thin film silicon is deposited from gaseous phase by Chemical Vapour Deposition (CVD) techniques in the form of hydrogenated amorphous, nanocrystalline or microcrystalline silicon thin films of different short- and long-range crystallographic order.

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2.1. Hydrogenated Amorphous Silicon

Amorphous silicon (a-Si) differs from crystalline silicon by the lack of long-range order in the crystallographic structure. Disordered silicon atoms in a-Si show many broken bonds that need to be passivated by hydrogen atoms to exhibit semiconductor properties. The content of bonded hydrogen can reach even 10% in the device-quality a-Si:H. The disorder leads to high optical absorption for the photon energies exceeding the optical band gap (1,7 eV). However, the disorder causes high density of localized states (band-tail and mid-gap states) hampering the photogenerated charge carrier transport and enhancing the recombination losses. Due to Staebler-Wronski effect [4] the electrical properties of a-Si:H deteriorate under prolonged illumination since during the light soaking additional (metastable) defects are created and stabilized a-Si:H solar cells may lose up to 20% in conversion efficiency.

a-Si:H is deposited from radicals of silane (SiH_4) using Plasma Enhanced Chemical Vapour

Deposition (PE-CVD) at low temperatures of the substrate ($T < 250 \text{ }^\circ\text{C}$) [9]. Plasma is generated by capacitive coupling of electrodes in the vacuum chamber using a radio-frequency generator. Typically 13,56 MHz frequency (or higher) is used. PE-CVD enables homogeneous deposition of thin layers over large areas (Gen 8.5 chambers allow deposition over $2,2 \times 2,6 \text{ m}^2$) boosting the throughput and decreasing the equipment cost. Alternative deposition techniques are Hot-Wire CVD (HW-CVD) or Expanding Thermal Plasma CVD (ETP-CVD) [9]. a-Si:H can be easily doped with phosphorous atoms by adding gas PH_3 to silane (SiH_4) to deposit an n-doped layer or with boron atoms by adding gas B_2H_6 or $\text{B}(\text{CH}_3)_3$ to silane to deposit a p-doped layer. Usually, separate chambers are used for deposition of p-, i- and n- layers to prevent cross-contamination. In in-line deposition systems without a vacuum break the number of deposition chambers reflects the layer thickness ratios. Since the i-layer is the thickest, most chambers are used for sequential deposition of it.

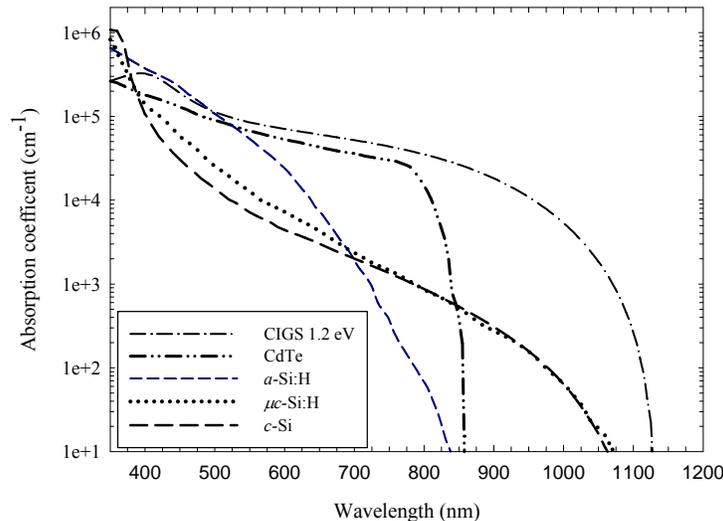


Figure 1. Wavelength-dependent absorption coefficients of selected inorganic PV materials.

The band gap of a-Si:H is not exactly defined. Mobility band gap (E_{μ}) and optical band gap (E_{opt}) are used instead. E_{opt} can be moderately varied by changing the hydrogen content (by changing hydrogen dilution during the deposition) and much more by alloying it with carbon (by adding methane (CH_4) to silane) or oxygen to enhance the E_{opt} ($\text{a-Si}_{1-x}\text{C}_x\text{:H}$ or $\text{a-Si}_{1-x}\text{O}_x\text{:H}$) or with germanium (by adding germane (GeH_4) to silane) to reduce the E_{opt} ($\text{a-Si}_{1-x}\text{Ge}_x\text{:H}$) depending on the fraction x [10].

Creative efforts in material research, device physics and process engineering have led to stabili-

zed efficiencies exceeding 10%. a-Si:H-based solar cells are made in single-junction, tandem-junction or triple-junction structure. Unlike the c-Si pn or np single-junction structure, the a-Si:H single-junction cells have the pin structure (Fig. 2). An undoped (intrinsic) layer is placed between a very thin but highly doped p- and n-layer, so that the space charge region widens and the presence of built-in electric field (\mathcal{E}_{bi}) extends across the whole pin structure. In this way, photogenerated carrier collection is aided by the \mathcal{E}_{bi} that mitigates the low carrier lifetimes in a-Si:H and the collection is drift driven (in contrast to

diffusion driven collection in c-Si cells). Biasing the a-Si:H cell with forward voltage lowers the electric field and leads to voltage-dependent photocurrent, resulting in a lower fill-factor and open-circuit voltage than anticipated.

Usually, single junction a-Si:H solar cells can be found in two configurations: superstrate p-i-n or substrate n-i-p. In case of the superstrate configuration (Fig. 2a) a transparent substrate covered with the transparent conductive oxide (TCO) as the front contact (FC) is located at the front side where incident light enters the cell. a-Si:H layers are deposited in a sequence of p-doped (p-layer), undoped intrinsic (i-layer) and an n-doped (n-layer). Finally, back contact (BC) is sputtered on top of the n-layer. In case of the substrate configuration (Fig. 2b) the deposition sequence is the opposite and front TCO layer is sputtered the last. In both configurations p-layer is located at the front part of the structure, where photogenerated carriers are predominantly generated, to enable shorter path for holes to reach the plus contact, since holes exhibit in a-Si:H typically 10-times lower mobility than free electrons. To transmit as much incident light as possible to the i-layer, the optical band-gap of the p-layer is usually increased by carbon (or oxygen) alloying that builds an effective electric field for electrons in the vicinity of the p/i interface that accelerates them in addition to the E_{bi} across the p-i-n structure toward the minus (back) contact.

To mitigate the photocurrent dependency on forward voltage and also the light-induced degradation, the a-Si:H cells should be as thin as possible, while optically to absorb the whole incident light. This calls for enhanced light confinement in a-Si:H solar cells. One of the most efficient light trapping techniques is light scattering at rough (textured) interfaces in nanometer range. The introduction of light scattering may enhance the absorption in the

single layer with refractive index n up to factor $4 \times n^2$ [11]. Assuming an average value of $n = 4$ for longer wavelengths in intrinsic a-Si:H material, the enhancement can reach the enviable number of 64 (in theory). In practice, the enhancement factors are still much below the ideal values, motivating several groups to investigate and optimize light scattering in a-Si:H as well as other thin-film silicon structures. Despite the complexity of scattering process in multilayer structures some optical models are capable of investigation and optimization of structures with rough and flat interfaces [SunShine [12], ASA [13], Cell [14]].

The optical optimisation is even more important for the tandem- and triple-junction cells, where the subcells are fabricated in a stacked structure and are electrically and optically connected in a series (Fig. 3). Due to a long-range disorder there is no need for lattice matching (in contrast to multijunction crystalline III-V solar cells). The fundamental concept underlying multijunction solar cells is multi band-gap approach that enables spectrum splitting. The top subcell absorbs the shorter wavelength part of the solar spectrum and transmits the longer wavelength part to the bottom subcell. Since the subcells are connected electrically in a series, the short-circuit currents of both subcells should be matched, while the open-circuits (V_{oc}) of the subcells will sum up. It is the higher V_{oc} of the top cell that brings the enhancement of the conversion efficiency, since more incident solar power is converted into electricity. Until the late 1990s tandem and triple-junction cells were made of a-SiGe low band gap absorbers, but since the discovery of nanocrystalline or microcrystalline silicon material by the group of IMT Neuchatel headed by Prof. A. Shah [15] the tandem micromorph cell with a-Si:H wide bandgap top cell and μ c-Si:H low band-gap bottom cell has been widely studied and optimized.

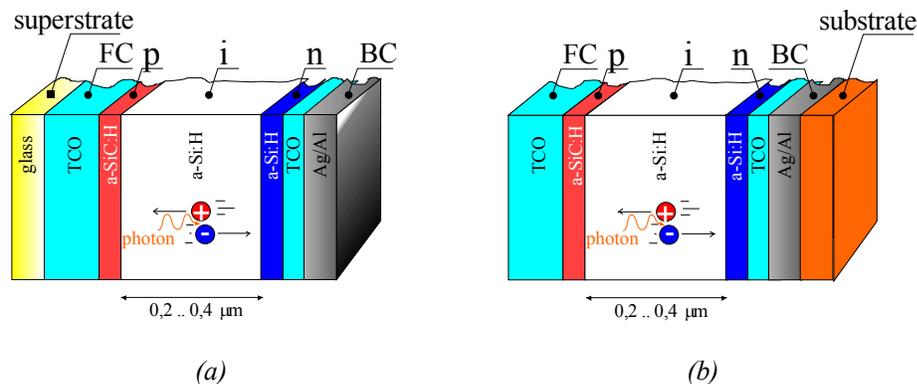


Figure 2. Schematic view of (a) superstrate p-i-n and (b) substrate n-i-p a-Si:H solar cell.

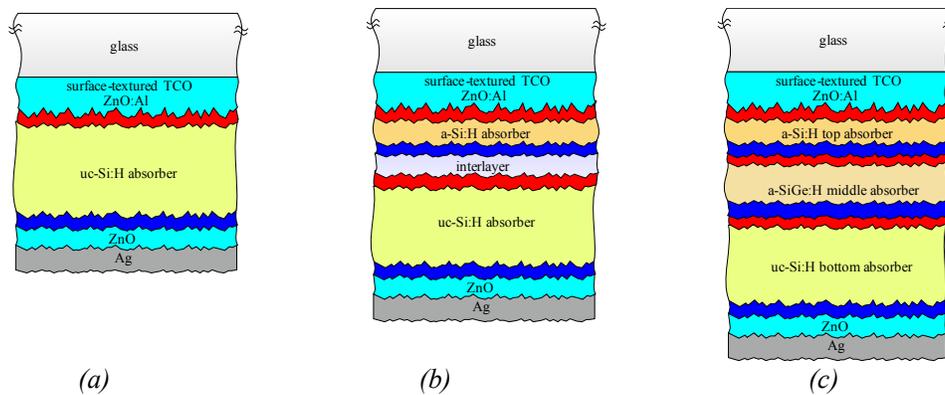


Figure 3. Schematic view of three advanced superstrate cell structures: (a) single-junction p-i-n $\mu\text{c-Si:H}$, (b) double-junction a-Si:H/ $\mu\text{c-Si:H}$ with interlayer as intermediate reflector and (c) triple-junction a-Si:H/a-SiGe:H/ $\mu\text{c-Si:H}$. All structures use random nanotextured front TCO ($\text{SnO}_2\text{:F}$, ZnO:Al or ZnO:B) and a ZnO/Ag back reflector and contact.

2.2. Hydrogenated Nanocrystalline and Microcrystalline Silicon

Hydrogenated nanocrystalline silicon (nc-Si:H) and microcrystalline silicon ($\mu\text{c-Si:H}$) have been intensively studied in the past decades. At first they have been used as improved doped layers in a-Si:H solar cells due to better effective doping and lower activation energy rendering higher ϵ_{bi} and V_{oc} . In 1994 Meier et al [15] reported the fabrication of $\mu\text{c-Si:H}$ -based p-i-n solar cell using very high frequency PE-CVD that exhibits extended conversion efficiency in the longer wavelength range (up to 1100 nm) rendering much higher short-circuits current. The major benefit of $\mu\text{c-Si:H}$ solar cells was almost no degradation upon light soaking. However, the absorption coefficient of $\mu\text{c-Si:H}$ is much lower (due to the indirect band gap of crystals) and thus the i-layer needs to be much thicker. This calls for higher deposition rates. The biggest progress was achieved in using VHF PE-CVD (40-100 MHz) combined with high-power and high-pressure regime [16]. Single junction p-i-n $\mu\text{c-Si:H}$ cells with over 9 % conversion efficiency have been fabricated at deposition rates of 2 nm/s [17].

Another strategy is to use efficient light trapping by advanced photon management approaches. Efficient light scattering within the cell, which can boost the photocurrent generation, can be achieved by optimised random or periodic interface textures [18]. Recently, the role of metal nano-particles in thin-film solar cell structures has been also investigated in view of improved light trapping [19]. To minimise optical losses, antireflective coatings and advanced back reflectors are employed (photonic crystal structures [20] and diffusive dielectric materials [21]). In multi-bandgap multi-junction cells, light trapping needs to be accurately controlled, sin-

ce the photogenerated current in subcells needs to be matched. For efficient spectrum harvesting, the concept of wavelength-selective intermediate reflectors has been investigated [22] and modification of doped layers to efficient intermediate reflectors has been demonstrated [23]. Research group of Oerlikon Solar has recently successfully launched the “think thin” concept where they succeeded in reducing the thickness of the whole micromorph® a-Si:H/ $\mu\text{c-Si:H}$ cell down to 1 μm while maintaining the efficiency above 10%. This approach is based on advanced light management that in production leads to less material and energy consumption and even more important to higher throughput and lower cost of equipment [<http://www.oerlikon.com/solar/thinfab/>].

3. POLYCRYSTALLINE MATERIALS – CHALCOPYRITES, KESTERITES, CdTe

3.1. Copper Indium Selenide, its alloys and related chalcopyrites and kesterides

CuInSe_2 (CIS) belongs to selenide minerals and exhibits a chalcopyrite structure with a tetragonal unit cell. CuInSe_2 can be arbitrary alloyed with CuGaSe_2 forming $\text{Cu}(\text{In}_{1-x}\text{Ga}_x)\text{Se}_2$. Alloying enables modification of the energy bandgap (E_g) from 1.04 eV (without Ga, $x = 0$) to 1.68 eV (without In, $x = 1$) following the empirical formula [24]:

$$E_g = 1,04 \text{ eV} + 0,39 \text{ eV} \times x + 0,26 \times x^2 \quad (1)$$

A remarkable property of CIGS layers is their insensitivity to morphology and size of grains that indicates no significant recombination loss at grain boundaries. This applies to for grains of half micron diameter or larger. It is still not clarified whether this is due to inherent passivation (Na or O) or Cu

vacancies at the grain surfaces cause valance band offset [25]. The microstructure of $\text{Cu}(\text{In}_{1-x}\text{Ga}_x)\text{Se}_2$ is influenced by substrate and the presence of Na (larger CIGS grains and preferred orientation of $\langle 112 \rangle$).

A variety of deposition methods (evaporation, reactive sputtering, closed-space sublimation, chemical bath deposition, spray pirolysis) has been successfully applied to prepare $\text{Cu}(\text{In}_{1-x}\text{Ga}_x)\text{Se}_2$ thin films. On the one hand physical vapour deposition in one step – elemental co-evaporation of Cu, In, Ga and Se to a heated substrate (450-600 °C) delivers best device-quality films. On the other, a two-step deposition process that consists of low-cost and low-temperature deposition of metal layers (co-sputtering, electrodeposition, ink or spray techniques) followed by the annealing step in H_2Se atmosphere has been successfully used in commercial manufacturing. To shorten the two-step approach a rapid thermal annealing in the second phase is usually applied. The thin films (typically from 1 to 2 μm thick) are polycrystalline and p-type semiconductor.

A typical CIGS solar cell is shown in Fig. 4a. The best ohmic contact to $\text{Cu}(\text{In,Ga})\text{Se}_2$ is formed with Molibdenum on soda-line glass. The absorber $\text{Cu}(\text{In}_{1-x}\text{Ga}_x)\text{Se}_2$ layer forms a heterojunction with a thin CdS layer that is n-type with a much higher bandgap. The growth of CdS thin films by chemical

bath deposition (CBD) allows ion-by-ion growing, resulting in a dense homogeneous film of CdS with crystallites in the ten nanometer range. During the CdS deposition, the surface of $\text{Cu}(\text{In,Ga})\text{Se}_2$ grains is exposed to chemical etching by ammonia and intermixing of Se and S on both sides of the hetero-interface may takes place, resulting in high quality np heterojunction and consequently solar cell. Although the Cd content in CIGS solar cells is low, regulation of Cd usage in electronic equipment and even more the concern about investors' attitude toward Cd has initiated development of Cd-free n-type buffer layers, in particularly the ones with a higher band-gap than 2,4 eV. Numerous wide-gap materials and deposition methods (CBD, atomic layer deposition, metal-organic CVD) have been investigated and the most promising buffer layers are $\text{Zn}(\text{S},\text{O},\text{OH})$ or $\text{Zn}(\text{Se},\text{O},\text{OH})$, $\text{In}(\text{OH},\text{S})$, In_2S_3 . To protect the buffer layer from harsh conditions during sputtering of $\text{ZnO}:\text{Al}$ a high-resistive undoped ZnO layer is deposited. It also mitigates the effect of inhomogeneities (e.g. weak-diode micro-cells) on overall performance [26].

A band diagram of $\text{ZnO}/\text{CdS}/\text{Cu}(\text{In,Ga})\text{Se}_2$ is shown in Fig. 4b. The conduction band offset (ΔE_c) at the $\text{CdS}/\text{Cu}(\text{In,Ga})\text{Se}_2$ interface causes the type inversion and the buried np junction with space charge region that is shifted toward the back.

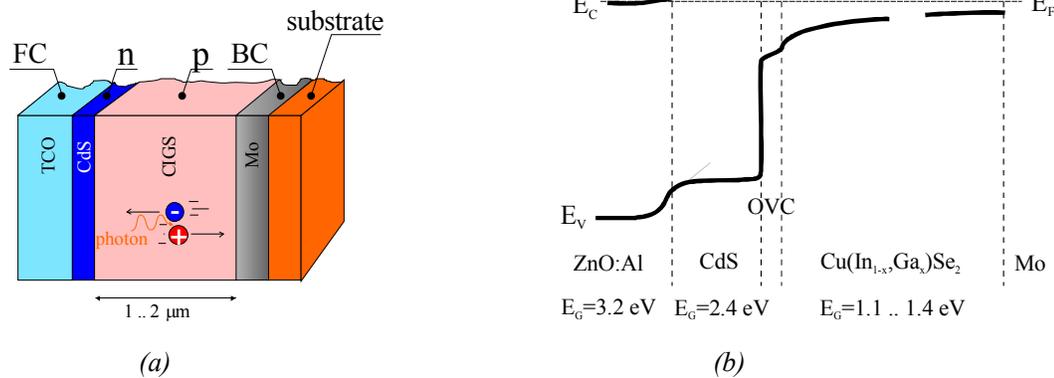


Figure 4. Schematic view of (a) substrate heterojunction np CdS/CIGS solar cell and (b) band-diagram of $\text{ZnO}/\text{CdS}/\text{CIGS}$ structure.

Soda-lime glass is a common substrate material, but recently A. Tiwari with co-workers has made a remarkable progress on flexible substrates with cell efficiencies under STC of 18.7% for polymer foil and above 17,7% for steel foil substrate [27].

Band-gap grading in $\text{Cu}(\text{In,Ga})\text{Se}_2$ solar cells can be achieved by a non-uniform Ga/In ratio thro-

ughout the film thickness. Numerical modeling tools have been used to clarify the best band-gap engineering approach and quantify the benefits of band-gap grading [28]. M. Gloecker [29] concluded that in standard thickness CIGS cells the benefit is smaller than commonly believed, while thin-absorber cells derive more substantial benefit.

In respect to primary material supply for large-scale production of CIGS solar cells, the potential bottleneck is the In supply. This limiting factor can be mitigated by thinner CIGS cells or by Indium-free alloys of which kesterite family of materials (composed of Cu, Zn, Sn and Se or S, acronym CZTSS) is the most promising and record CZTSS cells have already exceeded efficiencies of 9.5 % [30].

Recently the first gigawatt CIGS production plant has been put in operation by Frontier Solar [31]. The world's largest CIS PV production plant came on line in February, 2011. The Kunitomi Plant with 750 employees has fully-automated production lines inside the building area of 158,000 m². Their mass-production thin-film CIGS PV modules are Cd-free and Pb-free (RoHS compliant) and exhibit STC efficiency from 11.4% to 12.6% (in 2011).

3.2. Cadmium Telluride

Cadmium Telluride (CdTe) is unique among II-VI compounds in that it exhibits the lowest melting temperature (1365 K), highest average atomic number, and largest lattice constant (0,647 nm). Electronically, CdTe exhibits amphoteric semiconducting behaviour, making it possible to dope both intrinsically and extrinsically (by impurities) to n- or p-type semiconductor. CdTe is a direct band gap of 1.5 eV at 300 K with a weak temperature coefficient of about -0.4 eV/K.

Native defects and impurities (substitutional or interstitial) in CdTe produce localized shallow or deep trap states within the band gap and hence change the electronic properties. Acceptor doping (p-type) is less efficient than the donor doping (n-type). The desired electronic properties of polycrystalline CdTe are obtained by activation treatments (CdCl₂, O₂ and Cu) that activate or passivate native defects and help in rearrangement to larger grains that suppress recombination and thus improve effective carrier lifetime.

Similarly to CIGS, a variety of deposition methods have been successfully applied to deposit CdTe thin films [32]. The most successful among them so far are Close Space Sublimation, Physical Vapour Deposition and Vapour Transport Deposition, where condensation of Cd and Te₂ vapours on a heated substrate takes place. The electrodeposition method is among the most successful and low-cost..

A typical CdTe solar cell is deposited on glass/SnO₂:F superstrate (schematically shown in Fig. 6.a), although recently also ZnO:Al/i-ZnO has been successfully used as the front contact [33]. Significant progress has been achieved in substrate configuration using metal foils [27]. A good ohmic back

contact to the CdTe absorber is not straightforward, but requires a CdCl₂ activation treatment at elevated temperatures (around 400 °C) that forms Tellurium-rich p+-type grain surface. The post-deposition CdCl₂ treatment is not only beneficial to the back surface, but also reduces the CdTe cross-grain sheet resistance and results in reduced donor defect density in CdTe and CdS/CdTe interdiffusion thus remarkably impacting improved photocurrent and uniformity [34]. The Te-rich surface is covered by a Cu-Au or graphite electrical back contact (a conductive buffer layer to mitigate the Schottky barrier and undesirable band bending at the back, see Fig. 6.b) [35]. The back-contact barrier is essentially a second diode with opposite polarity to the np heterojunction. Its height can be reduced by addition of Cu, although high bulk-diffusion coefficient for Cu in CdTe may initiate potential stability issues in hot climate locations [36]. The barrier depletion width depends on the doping level of the CdTe. For a higher doping, the depletion width is small and it does not affect the band bending across the absorber, but usually lower doping needs to be compensated by a thicker CdTe absorber.

There is a significant difference in performance between today's highest efficiency of thin-film polycrystalline CdTe solar cells and single-crystal cells of comparable band gap (GaAs). The open-circuit voltage (more than 200 mV) presents the largest contribution to this difference.. CdTe open-circuit voltage and fill-factor are currently limited by low recombination lifetime (near 1 ns), low hole density (near 10¹⁴ cm⁻³), and the back-contact barrier. Two different strategies for improving voltage and performance have been suggested [37]. One of them is back surface field by increased doping toward the back contact and another one is the incorporation of a conduction-band energy barrier at the back surface of the solar cell that acts as an electron reflector, which can reduce the recombination due to the reduced electron flow to the back surface. The investigation has shown that the expanded-band-gap layer is the most efficient and practical mechanism for an electron reflector [38], however band-gap engineering in CdTe is much more difficult to implement than in CIGS solar cells and further R&D efforts are needed.

The CdTe solar cell level research has been translated to high-volume and fast-growing mass-production of CdTe PV modules. In 2009, First Solar Inc. became the first PV module manufacturing company to lower its manufacturing cost to \$1/W [39], which was a significant milestone in the race for PV cost competitiveness. At the end of 2009, First Solar had surpassed an annual production of 1 GW and has been the largest thin film PV

module manufacturer in the world since 2007. At the end of 2010, their annual production capacity (facto-

ries in USA, Germany and Malaysia) was 1.5 GW [40].

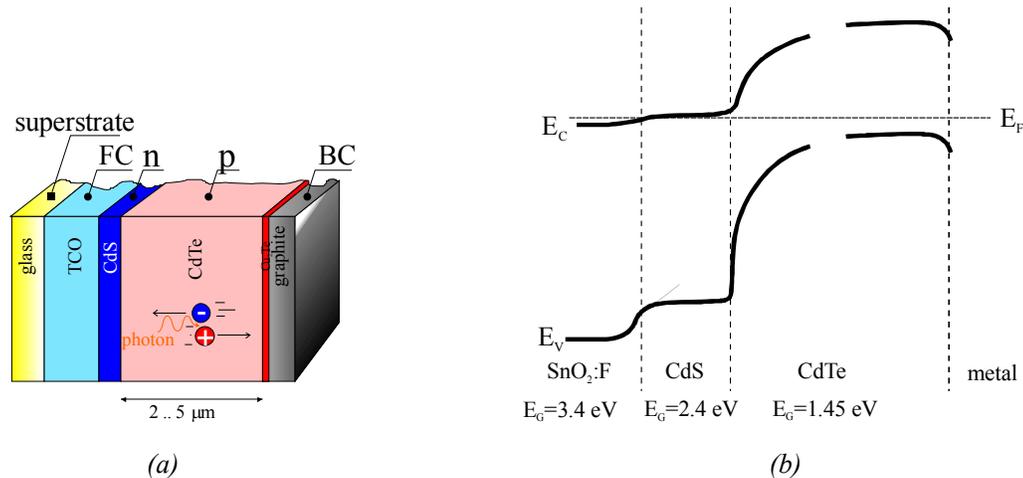


Figure 5. Schematic view of (a) superstrate heterojunction np CdS/CdTe solar cell and (b) band-diagram of ZnO/CdS/CdTe/metal structure.

4. CONCLUSION

Inorganic thin film photovoltaic materials have been reviewed. A variety of deposition techniques that allow deposition of inorganic thin films with small energy footprint and contribute to the promising low-cost photovoltaics as been presented. Small material consumption and high volume production of solar cells and photovoltaic modules make them competitive and sustainable contemporary PV technologies. A-Si:H, nc-Si:H and μ c-Si:H are at the forefront among the thin film silicon materials and CIGS and CdTe at the forefront of polycrystalline candidates whose state-of-the-art performance, bottlenecks and future challenges have been presented.

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САВРЕМЕНИ НЕОРГАНСКИ ТАНКОСЛОЈНИ ФОТОНАПОНСКИ МАТЕРИЈАЛИ И ТЕХНОЛОГИЈЕ

Сажетак: Неоргански танкослојни фотонапонски материјали доказују, и штавише, обећавају да ће њихова мала потрошња и ниска цијена бити покретач масовне производње соларних ћелија и фотонапонских модула. Приказане су типичне врсте савремених танкослојних фотонапонских материјала и технологија као и њихова најновија достигнућа и будући трендови.

Кључне ријечи: фотонапонски, соларна ћелија, полупроводник, неоргански материјал, танки слој.

